

Notice of References Cited	Application/Control No. 10/060,750		Applicant(s)/Patent Under Reexamination DEVINS ET AL.	
	Examiner RUSSELL GUILL		Art Unit 2123	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,199,126	03-2001	Auerbach et al.	710/68
*	B	US-2002/0183956	12-2002	Nightingale, Andrew Mark	702/120
	C	US-			
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	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Bergamaschi et al., "Designing systems-on-chip using cores", June 5, 2000, Proceedings of the 37th Design Automation Conference 2000, pages 420 – 425
	V	M. Morris Mano, "Computer System Architecture", second edition, 1982, Prentice-Hall, pages 272, 433
	W	Tom Shanley and Don Anderson, "ISA System Architecture", third edition, 1995, Addison-Wesley, pages 13, 16 – 19, 54, 125, 154, 241
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.